

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Jason Cheng; Cyrus Tsui; Satwant Singh; Albert Chan; Ju Shen; Clement Lee
Assignee: Lattice Semiconductor Corporation
Title: Programmable Logic Device With Enhanced Wide Input Product Term Cascading
Serial No.: 10/619,711 Filing Date: July 14, 2003
Examiner: James Cho Group Art Unit: 2819
Docket No.: M-9559-2P US Confirmation No.: 7093

Irvine, California
October 11, 2004

Via Facsimile to (703) 872-9306

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Attn: Examiner James Cho

OCT 11 2004

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-3514

PRELIMINARY AMENDMENT

Dear Sir:

Prior to examination of the above application, please amend the above-identified application as follows.

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